Reg. No.: Name:



Continuous Assessment Test - II (CAT 2) – December 2022

Programme]: E	3.Tech (CSE)	Semester	:	Winter 2022-23
Course	++=	Basic Electrical and Electronics Engineering	Code	:	BEEE102L
			Class	:	CH2022231700813,
			Number		CH2022231700363,
	: Basic				CH2022231700587,
					CH2022231700973,
					CH2022231700585,
					CH2022231700581
Faculty	: I	Dr.L.Premalatha, Prof.M.Krithika@AnbuDevi,	Slot	:	
	I	Prof.R.Srinivasan, Prof.Manish Kumar Dwivedi,		3	F1+TF1
	I	Prof.Richa Dubey, Prof.M.M.Sravani			
Time	: 9	90 minutes	Max. Marks	:	50

Answer all the Questions

Q.No.	Sub. Sec.	Question Description	Mark s	Unit No.	Level	Hot ?	criteri a
1.	THE THE WAS THE	Three coils are connected in star to a balanced three-phase, 3-wire, 440 V, 50 Hz supply and takes a line current of 10 A at 0.9 power factor lagging. (a). Calculate the resistance and reactance of the coils assuming they are series connected in each phase. (b). If the coils are delta connected to the same supply, calculate the line current and the real power. A circular ring is made of different materials A, B and C with lengths l ₁ =25 cm, l ₂ =15 cm and l ₃ = 25 cm. The cross-sectional area and relative permeability of A, B and C are 6cm ² , 8cm ² , 6cm ² and μ _{r1} =6000, μ _{r2} =8000, μ _{r3} =6000 respectively. Cross-sectional area and length of air gap are 6cm ² and 2mm respectively. Find the value of exciting current 'I' if number of turns (N) = 1000 turns, to setup a		2	M	N	2
2	THE PROPERTY OF THE PROPERTY O			3	H	Y	3
J. W.	m	flux of 1mWb.	£ Sq.	80/s			

3	Determine the voltage 'V _o ' in the given magnetically coupled circuit, using mesh analysis technique. $ \frac{100}{45^{\circ}} \text{ V} \text{ (1)} j8\Omega \text{ (1)} j8\Omega \text{ (1)} $	10	3	Н	N	3
4	For the following Boolean function $F(a,b,c,d) = \Sigma m (2,3,12,13,14,15).$ (a). Write the truth table, Boolean expression and draw the logic diagram using common gates. (5 marks) (b). Simplify the given function using K map and write the simplified Boolean expression. (5 marks) (c). Draw the logic diagram for the simplified Boolean expression using common gates. (5 marks) (d). Draw the logic diagram for the simplified Boolean expression using only NAND gates. (5 marks)		6	H	Y	3

17,32000